

FIG. 1a

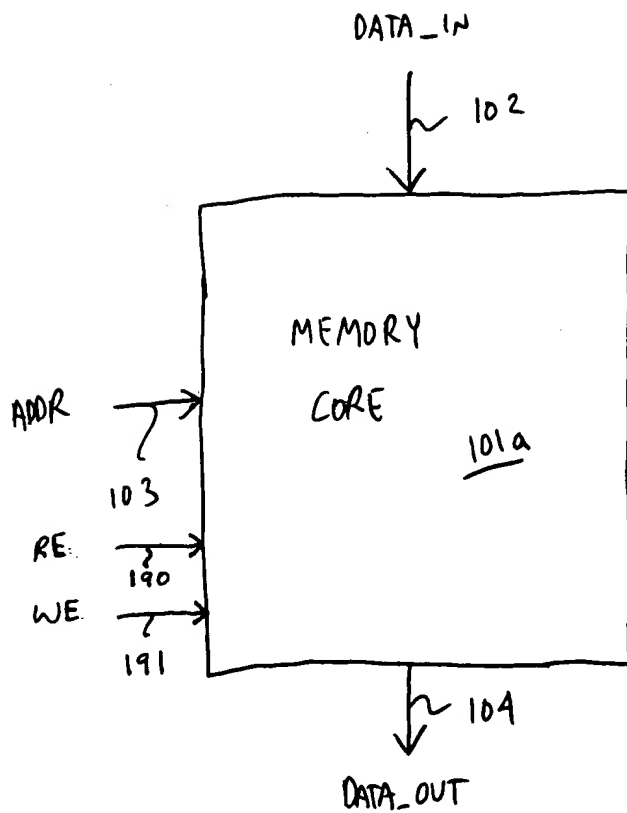


FIGURE 1a

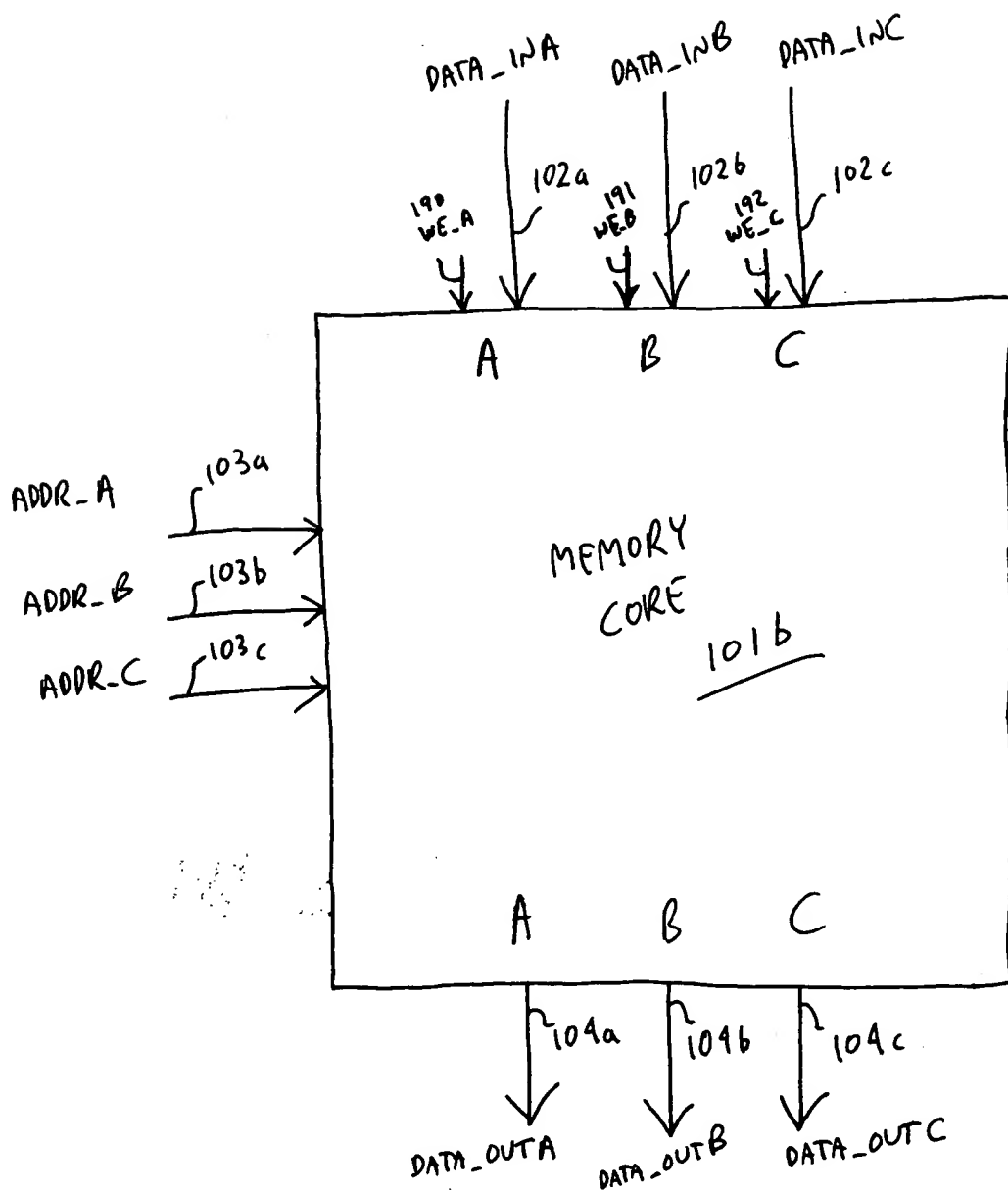


FIGURE 1b

FIGURE 2a

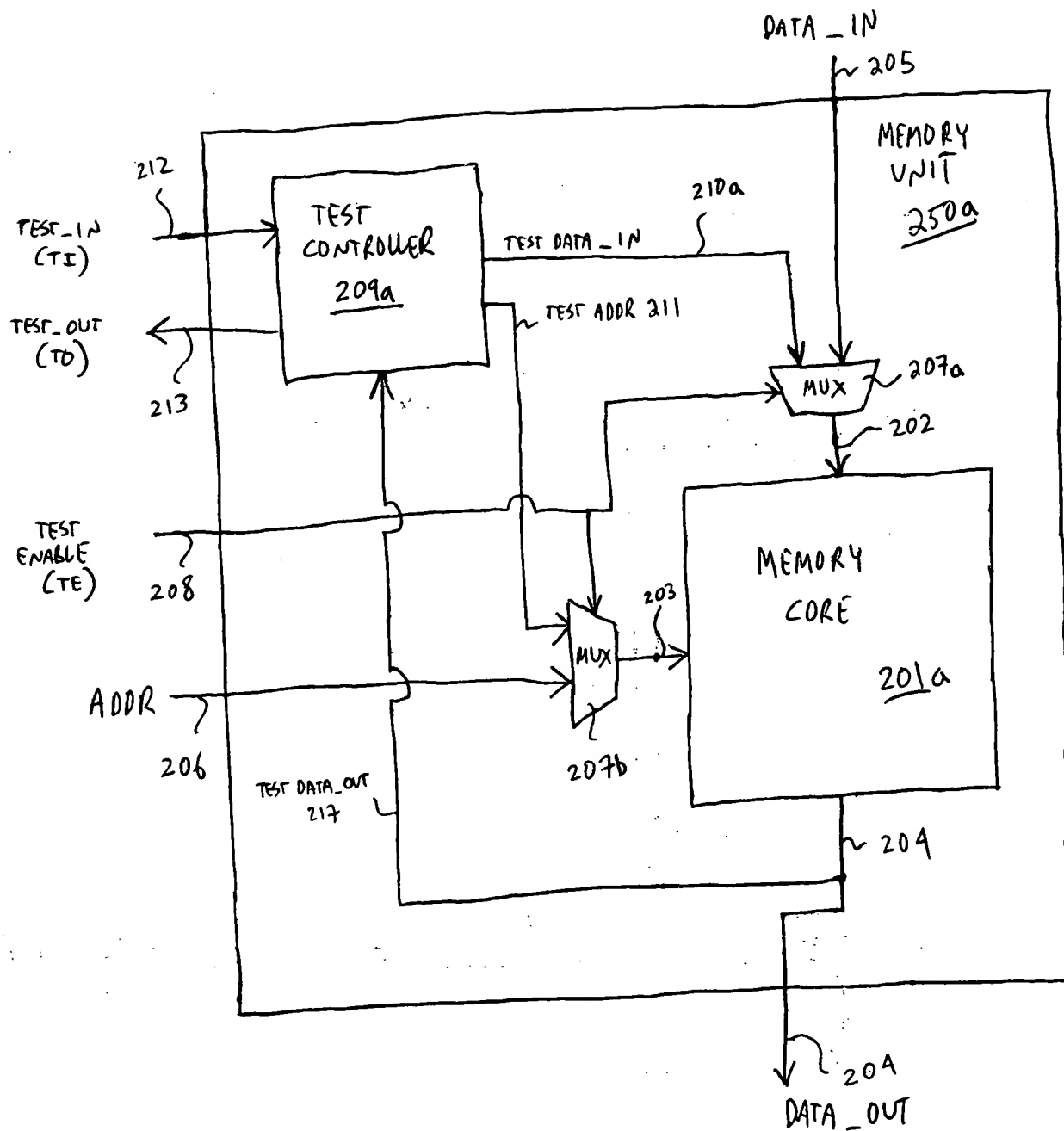


FIGURE 2a

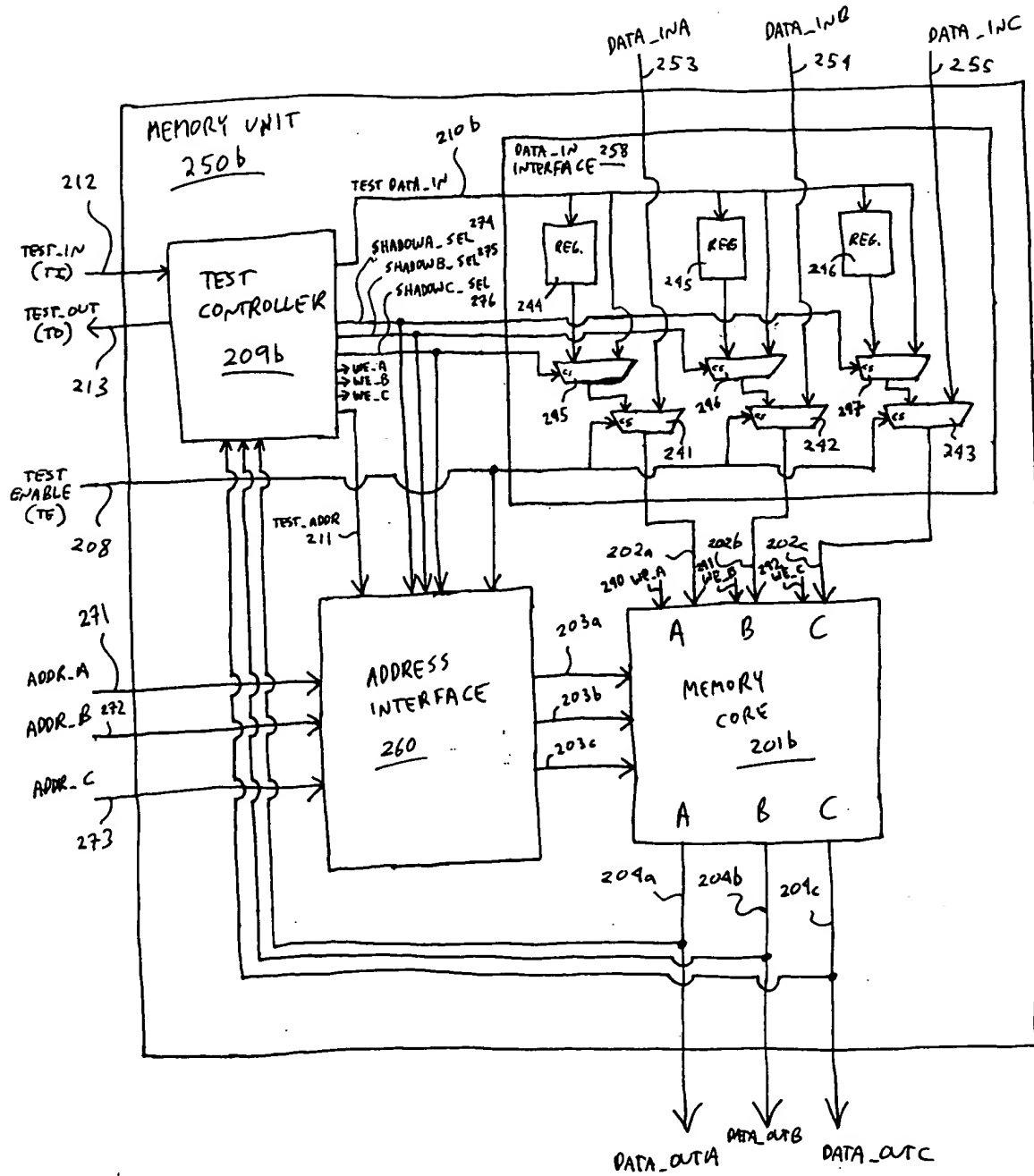
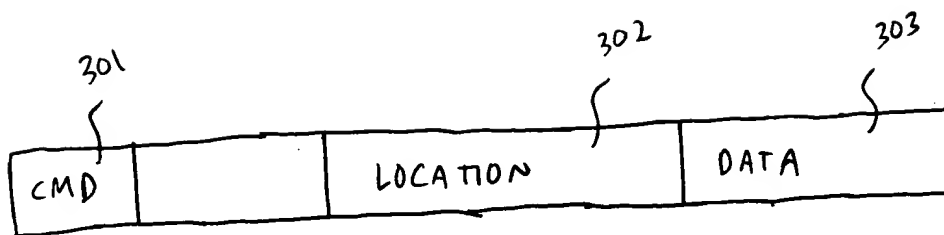


FIGURE 26



300

FIGURE 3

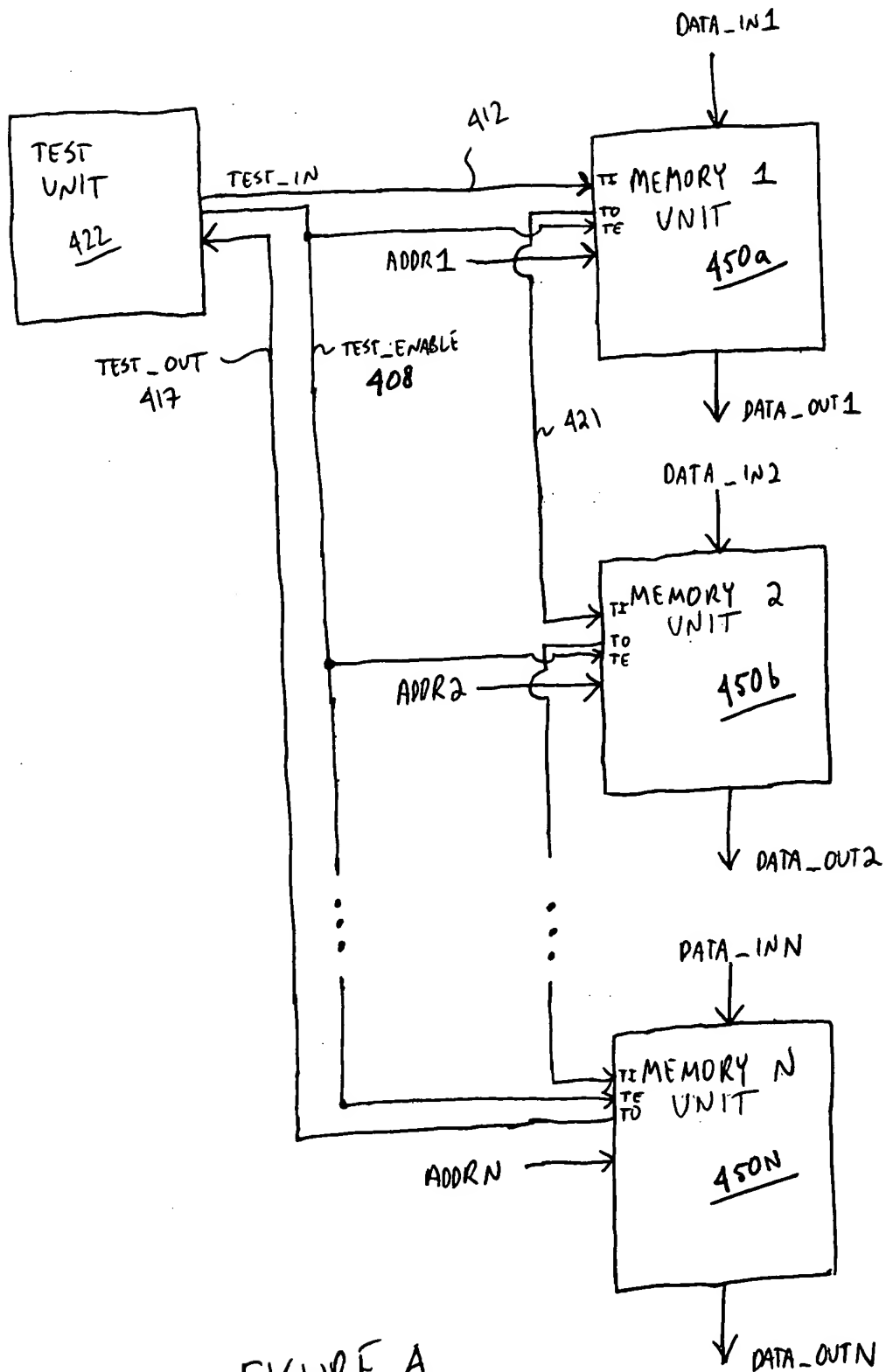


FIGURE A

Diagram illustrating a 1024-bit bus structure. The bus is divided into four segments. The first segment is labeled 'CMD' and is 501 bits wide. The second segment is unlabeled. The third segment is labeled 'SUB' and is 502 bits wide. The fourth segment is unlabeled. The total width is 1024 bits.

500

FIGURE 5

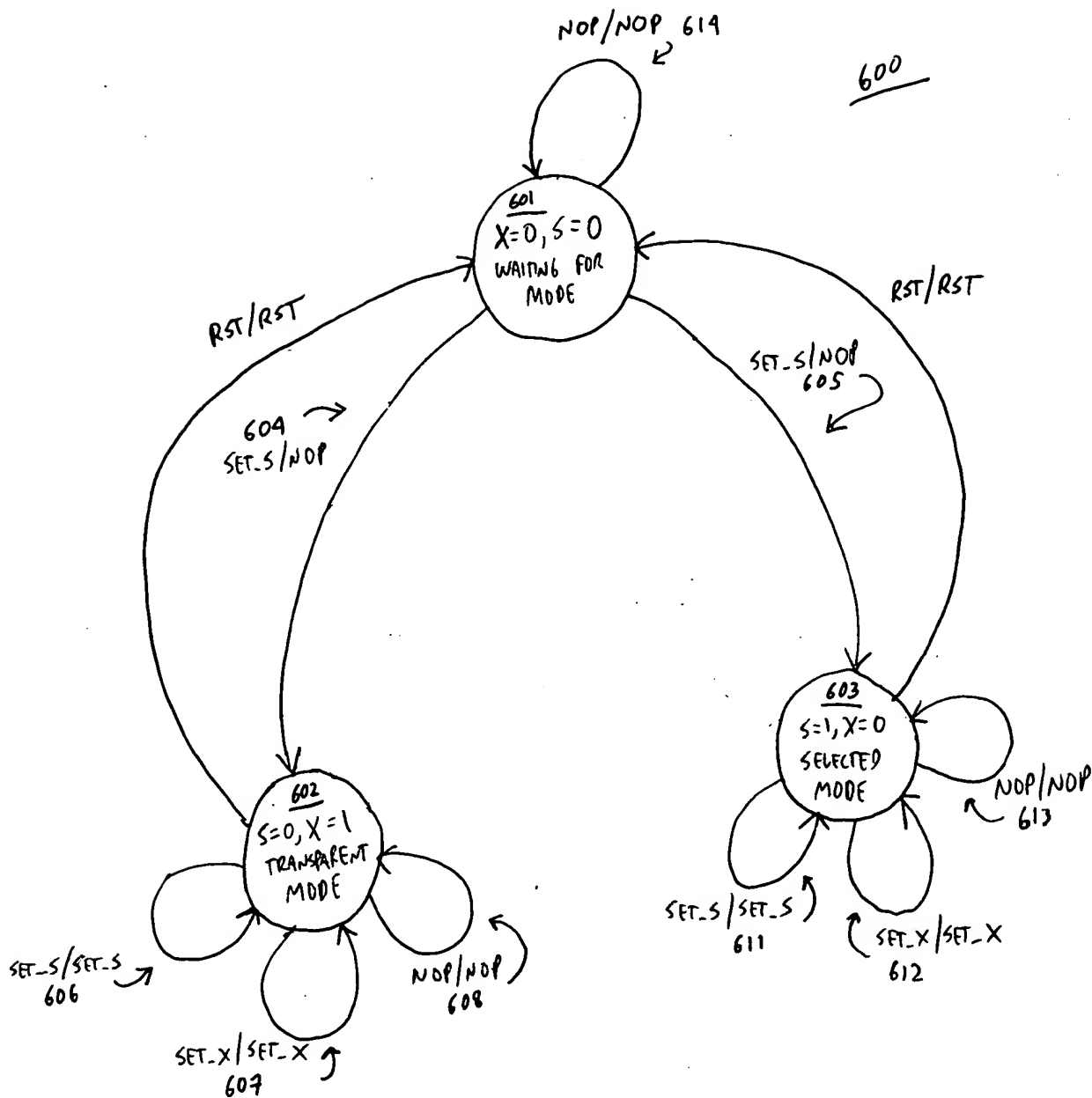


FIGURE 6



